

IN THE SPECIFICATION

Please amend the paragraphs of the specification as follows:

Please replace paragraph 0001 on page 1 with the following:

The present Application for Patent is a Continuation and claims priority to Patent Application No. 09/310,053 entitled "SYSTEM AND METHOD FOR PROVIDING AN ACCURATE ESTIMATION OF RECEIVED SIGNAL INTERFERENCE FOR USE IN WIRELESS COMMUNICATIONS SYSTEMS," filed May 11, 1999, now allowed U.S. Patent No. 6,661,832, issued on December 9, 2003 to Sindhushayana et al., and assigned to the assignee hereof and hereby expressly incorporated by reference herein.

Please replace paragraph 0037 on page 8 with the following:

The baseband-to-IF circuit 38 includes various components (not shown) such as digital-to-analog converters (DACs), mixers, adders, filters, shifters, and local oscillators. The baseband computer output signals 58 include both in-phase (I) and quadrature (Q) signal components that are [[90o]] 90° out of phase. The output signals 58 are input to digital-to-analog converters (DACs) in the analog baseband-to-IF circuit 38, where they are converted to analog signals that are then filtered by lowpass filters in preparation for mixing. The phases of the output signals 58 are adjusted, mixed, and summed via a [[90o]] 90° shifter (not shown), baseband-to-IF mixers (not shown), and an adder (not shown), respectively, included in the baseband-to-IF circuit 38.